

PATENT  
AUS9-2001-0301-US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Sang Hoo Dhong et al. )  
 )  
Serial No.: 09/915,437 ) Group Art Unit: 282

Filed: July 26, 2001 )  
FOR: METHOD OF LOGIC CIRCUIT )  
SYNTHESIS AND DESIGN )  
USING A DYNAMIC CIRCUIT )  
LIBRARY )  
Examiner: Binh C. Tat  
Confirmation No.: 7370

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

## RESPONSE TO OFFICE ACTION

This paper is filed in response to the Office Action mailed May 18, 2007, in the above-identified patent application and is filed within the three-month shortened statutory period for response set in the Office Action.

Please amend the application as follows: